

IA32 Size Optimisation

ToDo

x86 design

Size contra speed

Why?

How?

Security aspects

x86 Design

- The x86 processor architecture was developed by Intel in the 1970's
 - First appeared in the 8086 processor in 1978
- Uses a CISC design
 - Has very few registers, compared to RISC processors, which exist with up to 32 general purpose registers
 - From around mid-1990, the core of the Intel processors use an RISC architecture, but still uses its x86 CISC command set which is translated

Size Contra Speed

- N/A

Why?

- Primary reason; to get smaller binaries!

How?

- <Shown>

Security Aspects

- Smaller footprint lessen the risk of detection
- Polymorphism
- Known limits